

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	1	(clock adj signal adj multiplexer\$1) with (read adj address adj register\$1) with (write adj enable adj register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:43
L6	1	(clock adj signal adj multiplexer\$1) with (read with register\$1) with (write with register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:48
L8	2	(clock with multiplexer\$1) with (read adj address adj register\$1) with (write adj enable adj register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:43
L9	3	(clock with multiplexer\$1) with (read with register\$1) with (write with register\$1) with scan\$4 with clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:45
L10	3	(clock with multiplexer\$1) with (read with register\$1) with (write with register\$1) with scan\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:45
L11	5	((clock with multiplexer\$1) with (read with register\$1) with (write with register\$1)) same scan\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:45
L12	8	(clock adj signal adj multiplexer\$1) with scan\$4 with clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:49
L13	8	(clock adj signal adj multiplexer\$1) with scan\$4 with clock with signal\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:57
L14	1	(clock adj signal adj multiplexer\$1) with scan\$4 with clock with signal\$1 with read\$3 with writ\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:49
L15	1	(clock adj signal adj multiplexer\$1) with (scan\$4 adj test adj clock) with signal\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 14:57

L16	3	(clock adj signal adj multiplexer\$1) with scan\$4 with clock with signal\$1 with test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 15:17
L17	1	((clock adj signal adj multiplexer\$1) with scan\$4 with clock with signal\$1 with test\$3) and (read\$3 with register\$1) and (writ\$3 with register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 15:18
L18	1	((clock adj signal adj multiplexer\$1) with scan\$4 with clock with signal\$1) and (read\$3 with register\$1) and (writ\$3 with register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 15:18
L19	1	((clock adj signal adj multiplexer\$1) with scan\$4 with clock) and (read\$3 with register\$1) and (writ\$3 with register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 15:20
L27	4	(bypass adj multiplexer) with (bypass adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:46
L30	1	(lockup adj latch) with (address adj register\$1) with (hold adj time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:50
L31	1	(lockup adj latch) with (address adj register\$1) with (time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:50
L32	1	(lockup adj latch) with (address adj register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:50
L34	1	(latch) with (address adj register\$1) with (hold adj time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:51
L36	1	(latch) with (address adj register\$1) with (hold\$3 adj time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:51

L37	1	(latch\$1) with (address adj register\$1) with (hold\$3 adj time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:51
L38	1	(lockup adj latch) with (address adj register\$1) with (time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:52
L39	12	((latch) with (address adj register\$1) with (time)) same hold\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:54
L40	1	(lockup adj latch) with (address adj register\$1) with (time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:54
L41	1	(lockup adj latch) with (address adj register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:55
L42	4	(lockup adj latch) with (register\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:55
L43	1	(lockup adj latch) with (register\$1) with time	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 16:55
S2	1223	714/726.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 15:46
S3	1256	714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 15:47
S4	3203	714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 15:50

S5	446	326/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 15:50
S6	466	326/37.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 15:52
S7	2	((latch adj based adj random adj access adj memory) or LBRAM) and ((data adj register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3) and ((logic adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:13
S8	2	((latch adj based adj random adj access adj memory) or LBRAM) and ((data adj register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:01
S9	2	((latch adj based adj random adj access adj memory) or LBRAM) and ((data adj register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and (memory adj scan) and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:01
S10	2	((latch adj based adj random adj access adj memory) or LBRAM) and ((data adj register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and (memory adj scan\$4) and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:01
S11	2	((latch adj based adj random adj access adj memory) or LBRAM) and (data adj register\$1) and (data adj buffer\$1) and (latch adj array\$1) and multiplexer\$1 and ((memory adj scan) with test\$3) and ((logic adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:02
S12	2	((latch adj based adj random adj access adj memory) or LBRAM) and (data adj register\$1) and (data adj buffer\$1) and (latch adj array\$1) and multiplexer\$1 and ((memory adj scan\$4) with test\$3) and ((logic adj scan\$4) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:03

S13	4	((latch adj based adj random adj access adj memory) or LBRAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:11
S14	4	((latch adj based adj random adj access adj memory) or LBRAM) and scan\$4 and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:04
S15	2	((latch adj based adj random adj access adj memory) or LBRAM) and scan\$4 and test\$3 and register\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:05
S16	2	((latch adj based adj random adj access adj memory) or LBRAM) and scan\$4 and test\$3 and buffer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:05
S17	2	((latch adj based adj random adj access adj memory) or LBRAM) and scan\$4 and test\$3 and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:05
S19	3	(latch\$2 with ((random adj access adj memory) or RAM)) and ((data adj register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3) and ((logic adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:15
S20	3	(latch\$2 with ((random adj access adj memory) or RAM)) and ((data adj register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:14
S21	3	(latch\$2 with ((random adj access adj memory) or RAM)) and ((register\$1) same (data adj buffer\$1) same (latch adj array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3) and ((logic adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:15
S22	3	(latch\$2 with ((random adj access adj memory) or RAM)) and ((register\$1) same (buffer\$1) same (latch adj array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3) and ((logic adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:15

S23	3	(latch\$2 with ((random adj access adj memory) or RAM)) and ((register\$1) same (buffer\$1) same (array\$1) same multiplexer\$1) and ((memory adj scan) with test\$3) and ((logic adj scan) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:16
S25	41	(latch adj array) and (latch with RAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:16
S26	23	(latch adj array) and (latch with RAM) and (buffer same register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:17
S27	7	(latch adj array) and (latch with RAM) and (buffer same register same (latch adj array))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:17
S28	6	(latch adj array) and (latch with RAM) and (buffer same register same (latch adj array)) and (memory with scan\$4) and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:18
S29	3	(latch adj array) and (latch with RAM) and (buffer same register same (latch adj array)) and (memory with scan\$4) and test\$3 and (logic with scan\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:18
S30	6	(latch adj array) and (latch with RAM) and (buffer same register same (latch adj array)) and (memory with scan\$4) and test\$3 and logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:25
S32	4	(latch adj array) and (latch near RAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:27
S33	2	(latch adj array) and (latch near RAM) and (memory with scan\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:27
S37	2	(latch adj array) and (latch near RAM) and (memory with scan\$4) and register\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:29

S39	2	(latch adj array) and (latch near RAM) and (memory with scan\$4) and register\$1 and logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:29
S40	2	(latch adj array) and (latch near RAM) and (memory with scan\$4) and register\$1 and logic and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 16:42
S42	2	(latch adj array) and (latch near RAM) and (memory with scan\$4) and register\$1 and (logic with scan\$4) and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:13
S43	3	(logic adj scan\$4 adj test\$3) and (memory adj scan\$4 adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:16
S44	5	((logic adj scan\$4) with test\$3) and ((memory adj scan\$4) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:22
S45	3	((logic adj scan\$4) with test\$3) and ((memory adj scan\$4) with test\$3) and register\$1 and buffer\$1 and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:17
S46	5	((logic adj scan\$4) with test\$3) and ((memory adj scan\$4) with test\$3) and register\$1 and buffer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:23
S47	17	((logic adj scan\$4) same test\$3) and ((memory adj scan\$4) with test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:23
S48	3	((logic adj scan\$4) with test\$3) and ((memory adj scan\$4) with test\$3) and register\$1 and buffer\$1 and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:23
S49	8	((logic adj scan\$4) same test\$3) and ((memory adj scan\$4) with test\$3) and register\$1 and buffer\$1 and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:41

S50	8	(logic adj scan\$4) and ((memory adj scan\$4) with test\$3) and register\$1 and buffer\$1 and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:45
S51	3	(input adj data adj register\$1) same (input adj data adj buffer\$1) same array	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:46
S53	3	(data adj register\$1) same (data adj buffer\$1) same (latch adj array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:47
S54	29	(data adj register\$1) same (data adj buffer\$1) same (memory adj array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:51
S55	3	(data adj register\$1) same (data adj buffer\$1) same (memory adj array) same multiplexer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:48
S56	3	(data adj register\$1) same (data adj buffer\$1) same (memory adj array) same multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:48
S57	15	((data adj register\$1) same (data adj buffer\$1) same (memory adj array)) and multiplexer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 18:24
S59	1	((data adj register\$1) same (data adj buffer\$1) same (memory adj array)) and multiplexer\$1 and scan\$4 and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:55
S60	2	((data adj register\$1) same (data adj buffer\$1) same (memory adj array)) and multiplexer\$1 and scan\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:55
S61	2	((data adj register\$1) same (data adj buffer\$1) same (memory adj array)) and multiplexer\$1 and scan\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:56

S62	2	((data adj register\$1) same (data adj buffer\$1) same (memory adj array)) and multiplexer\$1 and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 17:56
S63	3	(multiplexer\$1 with (logic adj scan\$4) with (memory adj scan\$4)) and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 18:25
S64	3	(multiplexer\$1 with (logic adj scan\$4) with (memory adj scan\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 18:26
S65	3	(multiplexer\$1 same (logic adj scan\$4) same (memory adj scan\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 18:26
S66	25	(multiplexer\$1 same (logic same scan\$4) same (memory adj scan\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/19 18:26